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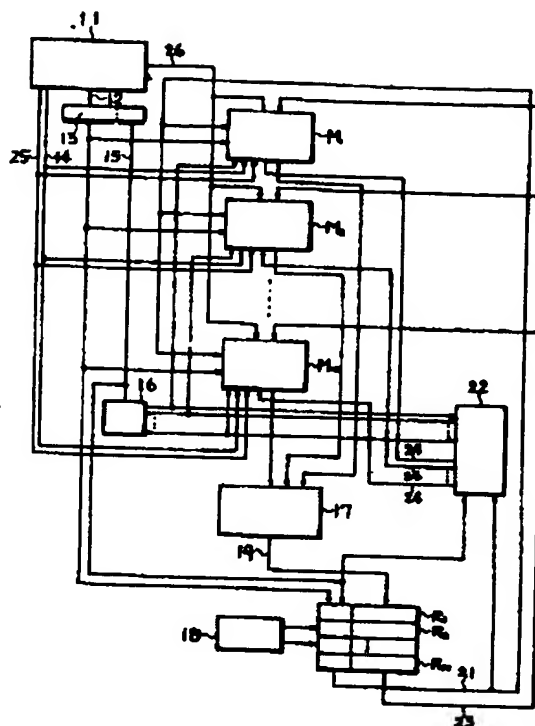
APPLICATION DATE : 16-10-78
APPLICATION NUMBER : 53127609

APPLICANT : NEC CORP;

INVENTOR : NISHIMURA HIROYUKI;

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TITLE : MEMORY CONTROL UNIT



ABSTRACT : PURPOSE: To rewrite data without lengthening the processing cycle by storing data with error correction code after error correction into a register and utilizing a non-access period to write data on error addresses.

CONSTITUTION: Data with error correction code is read out from memory units M_1, \dots , which are designated by address register 13 and selection circuit 16, and is sent to error detection correction circuit 17; and when uncorrectable errors are detected, circuit 17 corrects them, and corrected data with error correction code is stored in designated one of registers R_1, \dots . This corrected data word is rewritten into units M_1, \dots by write command circuit 22 at the timing when selector 16 does not designate addresses. As a result, data can be rewritten without lengthening the processing cycle.

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